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BIBDATASHEET

CONFIRMATION NO. 3910

Bib Data Sheet

SERIAL NUMBER 10/797,017	FILING DATE 03/11/2004 RULE	CLASS 257	GROUP ART UNIT 2814	ATTORNEY DOCKET NO. 501.36455CC2
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APPLICANTS

Tatsuyuki Saito, Tokyo, JAPAN;

Junji Noguchi, Tokyo, JAPAN;
 Hizuru Yamaguchi, Tokyo, JAPAN; Nobuo Owada, Tokyo, JAPAN;

** CONTINUING DATA *****

This application is a CON of 09/613,138 07/10/2000 PAT 6,731,007
 which is a CON of 09/139,410 08/25/1998 ABN *yes, PC*

** FOREIGN APPLICATIONS *****

JAPAN 9-234236 08/29/1997
 JAPAN 10-182813 06/29/1998 *yes, PC*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 05/31/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	JAPAN	40	21	2

Allowance *PC*
 Examiner's Signature *Crown* Initials *PC*

ADDRESS
 020457
 ANTONELLI, TERRY, STOUT & KRAUS, LLP
 1300 NORTH SEVENTEENTH STREET
 SUITE 1800
 ARLINGTON, VA
 22209-9889

TITLE
 Semiconductor integrated circuit device and fabrication process thereof

<input type="checkbox"/> All Fees
